

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s):	Kohichi Ohsumi (et al.)	Examiner:	Rick Kiltae, Chang
Serial No.:	10/709,752	Group Art Unit:	3726
Filed:	May 26, 2004	Attorney Docket No:	JP920030018US1
Title:	PRINTED CIRCUIT BOARD MANUFACTURING METHOD AND PRINTED CIRCUIT BOARD		

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**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Non-Fee Amendment  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is submitted in response to an Office Action mailed December 27, 2006 by the United States Patent and Trademark Office in connection with the above-identified Application. A response to this December 27, 2006 Office Action is due by January 27, 2006. Therefore, this paper is being timely filed.

**Remarks** begin on page 2 of this paper.